



Image

RCE/2817

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 09/811,647
Applicant : Vickram R. Vathulya
Title of Invention : CIRCUIT AND METHOD FOR INPUT SIDE
IMPEDANCE MATCHING OF A POWER
AMPLIFIER IN AN ELECTRONIC DEVICE
Date Filed : March 19, 2001
T.C./AU : 2817
Examiner : JONES, Stephen E.
Docket No. : US 010082

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

AMENDMENT

This RCE is responsive to the final Office Action dated October 17, 2003, in connection with the above-referenced patent application.

In this response, Applicant has further amended independent claims 1, 10 and 15, as reflected in a complete list of the claims starting on page 2.